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Sir:

Transmitted herewith for filing is the patent application of

INVENTOR OR APPLICATION IDENTIFIER: Myoung Jun SONG

FOR: APPARATUS AND METHOD OF INTERFACING VIDEO INFORMATION IN A COMPUTER SYSTEM

Enclosed are:

1. ☒ 30 pages of specification, claims, abstract

2. ☒ 4 sheets of FORMAL drawing.

3. ☒ 2 pages of newly executed Declaration & Power of Attorney (original).

4. ☒ Priority Claimed to Korean Appln. No. 31588/1999, whose entire disclosure is incorporated herein by reference.

5. ☐ Small Entity Statement.

6. ☐ Information Disclosure Statement, Form PTO-1449 and reference.

10. ☒ Authorization under 37 C.F.R. §1.136(a)(3).

11. ☐ Other:

7. ☒ Assignment Papers for LG Electronics Inc. (cover sheet, assignment & assignment fee).

8. ☒ Certified copy of Korean Patent Application No. 31588/1999 filed July 31, 1999.

9. ☒ Two (2) return postcards.

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CLAIMS AS FILED

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- ☐ This is a Continuation-in-part (CIP) of prior application No: \_\_\_\_\_ filed \_\_\_\_\_. Incorporation By Reference-The entire disclosure of the prior application is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.

☐ Amend the specification by inserting before the first line the sentence:  
-This application is a continuation-in-part of Application Serial No. \_\_\_\_\_ filed \_\_\_\_\_.--

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FLESHNER & KIM, LLP

*Daniel Y.J. Kim*

Daniel Y.J. Kim  
Registration No. 86,186  
Anthony H. Nourse  
Registration No. 46,121

Correspondence Address Below:

P.O. Box 221200  
Chantilly, VA 20153-1200  
(703) 502-9440

DYK:AHN/kam

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**APPLICATION FOR UNITED STATES LETTERS PATENT**

**INVENTORS:** Myoung Jun SONG

**TITLE:** APPARATUS AND METHOD OF INTERFACING VIDEO  
INFORMATION IN A COMPUTER SYSTEM

**ATTORNEYS:** FLESHNER & KIM, LLP  
& P. O. Box 221200  
**ADDRESS:** Chantilly, VA 20153-1200

**DOCKET NO.:** K-191

# APPARATUS AND METHOD OF INTERFACING VIDEO INFORMATION IN A COMPUTER SYSTEM

## BACKGROUND OF THE INVENTION

### 1. Field of the Invention

The present invention relates to a computer system, and more particularly to an apparatus and method of interfacing video information in a computer system.

### 2. Background of the Related Art

A related art computer system, as shown in Figure 1, includes a main body 1, such as a personal computer (PC) or a work station. The main body 1 performs various kinds of information operations and information processing, and generates a video signal for displaying corresponding processed contents on a display screen. A monitor 2 receives horizontal and vertical sync signals H-sync/V-sync and a RGB video signal outputted from the main body 1, as shown in Figure 2. The monitor thus displays a corresponding image, and performs various kinds of information interface with the main body 1 through serial data (SDA) and serial clock (SCL) lines of a display data channel (DDC).

The monitor 2 comprises a microcomputer 11 for detecting the display type of an input video signal by analyzing frequency information of the horizontal and vertical sync signals. The monitor 2 outputs a control signal so that the RGB video signal transmitted from the main body 1 is processed using the same format as the corresponding display

format, or in a form that corresponds to the closest factory mode. The monitor 2 further includes a video processing section 12 for processing the RGB video signal transmitted from the main body 1, to match the corresponding display type in accordance with the control signal from the microcomputer 11. Finally, a display section 13 displays an output of the video processing section on the display screen.

The operation of the related art computer system as constructed above will be described. First, the main body 1 of the computer system transmits the horizontal and vertical sync signals, as well as the RGB video signal to the monitor 2.

The microcomputer 11 in the monitor 2 detects the type of the input video signal in accordance with the frequencies of the horizontal and vertical sync signals, and controls the video processing section 12. This allows the input video signal to be displayed in a factory mode which is closest to the video type among factory modes predetermined in the monitor 2. The video processing section 12 processes the input video signal to match the factory type determined by the microcomputer 11, and displays the input video signal through the display section.

In the case of an analog type cathode ray tube (CRT) monitor, the factory mode corresponds to a table of the image type corresponding to the frequency information of the horizontal and vertical sync signals. Specifically, that includes an active video frequency for one horizontal period, a backporch time for one horizontal period, the total

number of horizontal lines for one vertical period, and a backporch time for one vertical period, among others.

For a digital type liquid crystal display (LCD) monitor, the factory mode corresponds to a table of the image type corresponding to the frequency information of the horizontal and vertical sync signals. Specifically, that includes the total number of dots for one horizontal period, the number of backporches for one horizontal period, the total number of horizontal lines for one vertical period, and the number of backporches for one vertical period, among others.

The main body 1 transmits only the RGB video signal and the horizontal and vertical sync signals to the monitor 2. This, however, is not enough for the monitor 2 to detect the type of the video signal transmitted from the main body 1. In order to actually display the video signal, such as the above-described video type, information regarding the active video frequency for one horizontal period and the like is required.

Accordingly, the factory mode, which has the video types corresponding to the representative horizontal and vertical sync signal frequencies listed in table, is applied to the respective monitor 2. The frequency of the horizontal and vertical sync signals transmitted from the main body 1 is compared with the frequency of the horizontal and vertical sync signals of the predetermined factory mode, and if the same horizontal and vertical sync signal frequency exists, the video signal is displayed using the video type corresponding to the horizontal and vertical sync signal frequency.

If, on the other hand, the same horizontal and vertical sync signal frequency does not exist in the table, the video signal is displayed using the video type corresponding to the frequency closest to the frequency of the horizontal and vertical sync signals transmitted from the main body 1.

5 Thus, according to the related art computer system, if the frequency of the horizontal and vertical sync signals transmitted from the main body does not coincide with that of the predetermined factory mode in the monitor, the position or size of the displayed picture will become abnormal, and a normal picture desired by the user cannot be outputted.

10 The above references are incorporated by reference herein where appropriate for appropriate teachings of additional or alternative details, features and/or technical background.

### SUMMARY OF THE INVENTION

15 An object of the invention is to solve at least the above problems and/or disadvantages and to provide at least the advantages described hereinafter.

Another object of the present invention is to provide a device and method of interfacing video information in a computer system that substantially obviates one or more of the problems due to limitations and disadvantages of the related art.

Another object of the present invention is to provide a device and method of interfacing video information in a computer system which can provide an accurately displayed picture regardless of the type of input video signal, by interfacing the display video information between the main body and the monitor.

5 To achieve these objects and other advantages, in whole or in parts, and in accordance with the purpose of the present invention, as embodied and broadly described, the apparatus for interfacing video information in a computer system according to one embodiment comprises a main body for outputting a video signal and information on a display type of the video signal, and a monitor for detecting the display type of the corresponding video signal in accordance with the display type information outputted from the main body, and displaying on a display screen the video signal outputted from the main body to match the detected display type.

10 In another embodiment of the present invention, there is provided an apparatus for interfacing video information in a computer system comprising a monitor for outputting video type information of the video signal that can be displayed by the monitor, and displaying on a display screen a video signal externally inputted in the corresponding video type, and a main body for converting a display type of the video signal to match the video type information outputted from the monitor, and outputting the video signal to the monitor.







To further achieve the above-described objects of the present invention in a whole or in parts, there is provided an apparatus for interfacing video information that includes a main body, which outputs a video signal and corresponding display information, and a monitor, which detects a display type of the corresponding video signal in accordance with the display information, and displays the video signal outputted from the main body in accordance with the detected display type.

To further achieve the above-described objects of the present invention in a whole or in parts, there is provided a video interface that includes a main body, which outputs a video signal through a video signal line, and outputs information relating to the video signal display type through a communication line, and a monitor, which detects the display type of the corresponding video signal in accordance with the display type information, and displays the video signal outputted from the main body in accordance with the detected display type.

To further achieve the above-described objects of the present invention in a whole or in parts, there is provided an apparatus for interfacing video information in a computer system that includes a main body, which outputs a video signal, a horizontal sync signal and a vertical sync signal at least one of which carries video signal display information, and a monitor, which detects the type of display for the corresponding video signal in accordance with the display information, and displays the outputted video signal in accordance with the detected display type.

To further achieve the above-described objects of the present invention in a whole or in parts, there is provided a video interface that includes a main body, which provides information relative to a display type of a video signal embedded in a video signal, a horizontal sync signal and a vertical sync signal, and outputs the video signal, the horizontal sync signal and the vertical sync signals, and a monitor which detects the display type of the corresponding video signal in accordance with the display type information outputted from the main body, and displays the video signal in accordance with the detected display type.

To further achieve the above-described objects of the present invention in a whole or in parts, there is provided an apparatus for interfacing video information in a computer system includes a monitor, which outputs data identifying a video type of a video signal that can be displayed by the monitor, and displays the video signal externally inputted in a corresponding video type, and a main body, which converts a display type of the video signal to match the video type data outputted from the monitor, and outputs the converted video signal to the monitor.

To further achieve the above-described objects of the present invention in a whole or in parts, there is provided a method of interfacing video information that includes transmitting video signal display type information for a main body to a monitor through one of the horizontal and vertical sync signals, a video signal, and communication data, and detecting a display type of the video signal transmitted from the main body using the

video signal display type information, and displaying the video signal to match the display type.

To further achieve the above-described objects of the present invention in a whole or in parts, there is provided a method of interfacing video information that includes transmitting display type information of the video signal in at least one of the horizontal sync signal and the video signal to a monitor, and detecting a display type of the transmitted video signal using the display type information.

To further achieve the above-described objects of the present invention in a whole or in parts, there is provided a method of interfacing video information that includes dividing display type information of a video signal, embedding the divided display type information into the horizontal sync signal and the vertical sync signal, respectively, transmitting to a monitor the horizontal and vertical sync signals having the divided display type information, along with the video signal, decoding and reassembling the display type information, and detecting a display type of the transmitted video signal using the reassembled display type information.

To further achieve the above-described objects of the present invention in a whole or in parts, there is provided a method of interfacing video information that includes transmitting display type information of a video signal from a monitor to a main body,

receiving the display type information and converting a type of video signal based on the received information to match a display type of the monitor, and displaying on the monitor the video signal transmitted from the main body.

To further achieve the above-described objects of the present invention in a whole or in parts, there is provided an apparatus for interfacing video information that includes a monitor which transmits information regarding a display type of a video signal through a display data channel, and a computer converting the display type of the video signal based on the information received from the monitor through the display data channel and transmitting the converted video signal with horizontal and vertical sync signals to the monitor.

To further achieve the above-described objects of the present invention in a whole or in parts, there is provided an apparatus for interfacing video information that includes a computer transmitting horizontal and vertical sync signals, serial data signal and serial clock signal through a display data channel and a video signal, and a monitor receiving the horizontal and vertical sync signals, serial data signal and serial clock signal through the display data channel and the video signal, wherein a display type information of the video signal is included in one of the serial data signal of the display data channel, and the horizontal sync signal.

Additional advantages, objects, and features of the invention will be set forth in part in the description which follows and in part will become apparent to those having

ordinary skill in the art upon examination of the following or may be learned from practice of the invention. The objects and advantages of the invention may be realized and attained as particularly pointed out in the appended claims.

### **BRIEF DESCRIPTION OF THE DRAWINGS**

5           The invention will be described in detail with reference to the following drawings in which like reference numerals refer to like elements wherein:

Figure 1 is a block diagram illustrating the construction of a related art computer system.

Figure 2 is a waveform diagram of horizontal and vertical sync signals and SDA/SCL signals of Figure 1.

Figure 3 is a block diagram illustrating the video information interfacing apparatus in a computer system according to a first embodiment of the present invention.

Figure 4 is a block diagram illustrating the video information interfacing apparatus in a computer system according to a second embodiment of the present invention.

15           Figure 5 is a waveform diagram showing the operation of the video information interfacing apparatus in a computer system according to the first embodiment of the present invention.

Figure 6 is a waveform diagram showing the operation of the video information interfacing apparatus in a computer system according to second and third embodiments of the present invention.

Figure 7 is a waveform diagram showing the operation of the video information interfacing apparatus in a computer system according to a fourth embodiment of the present invention.

Figure 8 is a drawing illustrating the format of the display type information according to one embodiment of the present invention.

### **DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS**

Reference will now be made in detail to the preferred embodiments of the present invention, examples of which are illustrated in the accompanying drawings. First, the video information interfacing apparatus in a computer system according to the first and second embodiments of the present invention will be described.

#### **First Embodiment**

As shown in Figure 3, the video information interfacing apparatus in a computer system according to the first embodiment includes a main body 31, such as a PC or a workstation, for example, which generates and outputs a RGB video signal and horizontal and vertical sync signals for displaying a picture. The main body 31 also outputs display type information of the RGB video signal through a display data channel (DDC

(SDA/SCL)) for interfacing various kinds of information. The apparatus further includes a monitor 32, for detecting a display type of the corresponding video signal in accordance with the display type information outputted from the main body 31, and displaying on a display screen the video signal to match the display type.

5           The monitor 32 comprises a control circuit 41, such as a microcomputer 41, which detects the type of the RGB video signal in accordance with the display type information transmitted from the main body 31. The microcomputer 41 outputs a control signal so that the RGB video signal will be processed in accordance with the corresponding display type. The monitor 31 further includes a video processing section 42, for processing the RGB video signal transmitted from the main body 31 in accordance with the control signal from the microcomputer 41. This allows the RGB video signal to be displayed on the display screen. Finally, the monitor 32 includes a display section 43 for displaying an output of the video processing section 42 on the display screen.

10           The operation of the video information interfacing apparatus according to the first embodiment of the present invention will now be described.

15           First, the main body 31 transmits the horizontal and vertical sync signals and the RGB video signal to the monitor 32. Simultaneously, the main body 31 includes the RGB video signal display type information in the serial data (SDA), horizontal sync signal, or RGB video signal, and transmits the display type information to the monitor 32.



The display type information preferably includes the number of dots for a horizontal period, the number of backporches for a horizontal period, the number of horizontal lines for a vertical period, and the number of horizontal lines of the backporch for a vertical period, among other information.

5       The microcomputer 41 in the monitor 32 detects the display type of the RGB video signal transmitted from the main body 31 in accordance with the display type information, and outputs a control signal to the video processing section 42. This allows the RGB video signal to be processed in accordance with the detected display type.

10       The video processing section 42 processes and outputs to the display section 43 the RGB video signal in accordance with the control signal from the microcomputer 31 so that the RGB video signal can be displayed. The display section 43 then displays the output of the video processing section 42 on the display screen.

### Second Embodiment

15       As shown in Figure 4, the video information interfacing apparatus according to the second embodiment includes a monitor 52, which outputs display type information of an RGB video signal that can be displayed by the monitor, such as a factory mode, for example. The monitor 52 also displays on a display screen an externally inputted RGB video signal in the corresponding display type. A main body 51 converts the display type of the RGB video signal to match the display type information outputted from the  
20       monitor 52, and outputs the RGB video signal to the monitor 51.

1 The monitor 52 includes a microcomputer 61, which detects the type of the RGB  
video signal transmitted from the main body 51 and outputs a control signal. The control  
signal allows the RGB video signal to be processed in the corresponding factory mode.  
A video processing section 62 processes the RGB video signal transmitted from the main  
5 body 51 in accordance with the control signal from the microcomputer 61 so that the  
RGB video signal can be displayed on the display screen. A display section 63 then  
displays an output of the video processing section 62 on the display screen.

The operation of the video information interfacing apparatus according to the  
second embodiment of the present invention will now be described.

10 First, the microcomputer 61 in the monitor 52 transmits information regarding the  
display type of the video signal, such as the display type of the predetermined factory  
mode, to the main body 51 through the serial data line (SDA).

15 Then, the main body 51 converts the display type of the RGB video signal to  
match the display type outputted from the monitor 52. In this way, the RGB video signal  
can be normally displayed in the monitor 52. The main body 51 then transmits the  
converted RGB video signal to the monitor 52 along with the horizontal and vertical sync  
signals.

20 The microcomputer 61 in the monitor 52 consequently detects the display type of  
the RGB video signal, and outputs the control signal to the video processing section 62,  
allowing the RGB video signal to be displayed in the corresponding type.

The video processing section 62 then processes and outputs the RGB video signal to the display section 63 in accordance with the control signal from the microcomputer 61. Thus, the RGB video signal can be displayed on the display screen, and the display section 63 displays the corresponding video signal on the display screen.

5 Hereinafter, the video information interfacing method in a computer system according to the first, second, third, and fourth embodiments of the present invention will be described.

### First Embodiment

According to the video information interfacing method in a computer system according to the first embodiment of the present invention, as shown in Figure 5, the main body synchronizes the display information of the RGB video signal with the vertical sync signal, carries the synchronized display type information on the serial data (SDA), and transmits to the monitor the serial data (SDA) along with the serial clock pulse (SCL). Accordingly, the monitor reads the serial data according to the serial clock pulse using the vertical sync signal as an enable signal.

The monitor then detects the display type information carried on the serial data, and displays the RGB video signal to match the corresponding type.

### Second Embodiment

According to the video information interfacing method in a computer system according to the second embodiment of the present invention, as shown in Figure 6, the

main body carries the display type information ② of the RGB video signal on any one of the horizontal sync signal, the R video signal, the G video signal, and the B video signal. In this way, the display type information is preferably synchronized with the vertical sync signal ③. The main body also carries the display type information on the horizontal sync signal, and transmits the display type information to the monitor with the clock pulse for recognizing the display type information included in the vertical sync signal. In Figure 6, ① denotes the horizontal sync signal period.

Accordingly, the monitor reads the display type information carried on the horizontal sync signal according to the clock pulse included in the vertical sync signal using the vertical sync signal as an enable signal. The monitor then detects the display type information, and displays the RGB video signal to match the corresponding type.

### **Third Embodiment**

According to the video information interfacing method in a computer system according to the third embodiment of the present invention, as shown in Figure 6, the main body divides the display type information of the RGB video signal, and carries divided display type information on the horizontal sync signal, R video signal, B video signal, or B video signal. By doing so, the display type information is synchronized with the vertical sync signal ③. The main body also transmits to the monitor the display type information with the clock pulse for recognizing the display type information included in the vertical sync signal.

For example, the main body may divide the display type information into two, and carry the two pieces of divided information on the horizontal sync signal and R video signal, respectively. Also, the main body may divide the display type information into four divided parts of display type information, and carry the parts on the horizontal sync signal, R video signal, G video signal, and B video signal, respectively. Then, the main body transmits the divided display type information to the monitor with the clock pulse for recognizing the display type information included in the vertical sync signal.

Accordingly, the monitor reads the divided display type information carried on the horizontal sync signal, R video signal, G video signal, or B video signal according to the clock pulse included in the vertical sync signal using the vertical sync signal as an enable signal.

The monitor then combines the divided display type information and detects the contents of the combined display type information, and displays the RGB video signal on the display screen to match the corresponding type.

#### **Fourth Embodiment**

According to the video information interfacing method in a computer system according to the fourth embodiment of the present invention, as shown in Figure 7, the monitor includes information regarding the display type of the video signal that can be displayed by the monitor, i.e., the factory mode information, in the serial data (SDA).

The monitor transmits the serial data (SDA) to the main body along with the serial clock pulse (SCL).

The main body then detects the display type information transmitted from the monitor, and converts the RGB video signal to match the display type. It does this by recognizing the display type of the video signal that can be displayed normally by the monitor, and then transmits the RGB video signal along with the corresponding horizontal and vertical sync signals to the monitor.

Accordingly, the monitor detects the display type of the RGB video signal using the horizontal and vertical sync signals transmitted from the main body, and displays data on the display screen in the factory mode, which matches the display type among the predetermined factory modes.

According to the first to fourth embodiments of the present invention, various types of the display type information can be provided. Referring to Figure 8, which exemplifies a type of the display type information, the information is composed of a total of 16 bits, including recognition codes of 2 bits and data of 14 bits.

The recognition code '00' represents information on the number of dots for one horizontal period. The recognition code '01' represents information on the number of backporches for one horizontal period. The recognition code '10' represents information on the number of horizontal lines for one vertical period. The recognition code '11'

represents information on the number of horizontal lines of a backporch for one vertical period.

The display type information is preferably classified into 4, but the total number of bits and the bit number of the recognition code can be varied according to the number of the display type information.

As described above, according to the apparatus and method of interfacing video information in a computer system according to the various embodiments of the present invention, an optimum picture state can be achieved and maintained, irrespective of the display type of the monitor input video, through the exchange of the display type information between the main body and the monitor. The reliability of products can thus be improved.

The foregoing embodiments and advantages are merely exemplary and are not to be construed as limiting the present invention. The present teaching can be readily applied to other types of apparatuses. The description of the present invention is intended to be illustrative, and not to limit the scope of the claims. Many alternatives, modifications, and variations will be apparent to those skilled in the art. In the claims, means-plus-function clauses are intended to cover the structures described herein as performing the recited function and not only structural equivalents but also equivalent structures.

**WHAT IS CLAIMED IS:**

1. An apparatus for interfacing video information, comprising:  
a main body, which outputs a video signal and corresponding display  
information; and

a monitor, which detects a display type of the corresponding video signal  
5 in accordance with the display information, and displays the video signal outputted from  
the main body in accordance with the detected display type.

2. A video interface, comprising:  
a main body, which outputs a video signal through a video signal line, and  
outputs information relating to the video signal display type through a communication  
line; and

a monitor, which detects the display type of the corresponding video signal  
in accordance with the display type information, and displays the video signal outputted  
from the main body in accordance with the detected display type.

3. The video apparatus as claimed in claim 2, wherein the communication line  
is a display data channel (DDC).



4. An apparatus for interfacing video information in a computer system, comprising:

a main body, which outputs a video signal, a horizontal sync signal and a vertical sync signal at least one of which carries video signal display information; and

5 a monitor, which detects the type of display for the corresponding video signal in accordance with the display information, and displays the outputted video signal in accordance with the detected display type.

5. A video interface, comprising:

a main body, which provides information relative to a display type of a video signal embedded in a video signal, a horizontal sync signal and a vertical sync signal, and outputs the video signal, the horizontal sync signal and the vertical sync signals; and

a monitor which detects the display type of the corresponding video signal in accordance with the display type information outputted from the main body, and displays the video signal in accordance with the detected display type.

6. An apparatus for interfacing video information in a computer system, comprising:

a monitor, which outputs data identifying a video type of a video signal that can be displayed by the monitor, and displays the video signal externally inputted in a corresponding video type; and

a main body, which converts a display type of the video signal to match the video type data outputted from the monitor, and outputs the converted video signal to the monitor.

7. The apparatus as claimed in claim 6, wherein the video type data of the video signal that can be displayed by the monitor is a factory mode predetermined during a manufacturing process of the monitor.

8. A method of interfacing video information, comprising:  
transmitting video signal display type information for a main body to a monitor through one of the horizontal and vertical sync signals, a video signal, and communication data; and

detecting a display type of the video signal transmitted from the main body using the video signal display type information, and displaying the video signal to match the display type.

9. The method as claimed in claim 8, wherein the display type information includes a recognition code for designating a kind of the corresponding display type information, and data corresponding to the recognition code.

10. The method as claimed in claim 9, wherein the recognition code is composed of two bits.

11. The method as claimed in claim 9, wherein the data includes a number of dots in a horizontal period, a number of backporches in the horizontal period, a number of horizontal lines in a vertical period, and a number of horizontal lines of a backporch in the vertical period.

12. A method of interfacing video information, comprising:  
transmitting display type information of a video signal in communication data, along with the horizontal and vertical sync signals from a main body to a monitor;  
and

5 detecting a display type of the transmitted video signal using the display type information, and displaying the video signal to match the display type.

13. The method as claimed in claim 12, wherein the display type information is synchronized with the vertical sync signal.

14. A method of interfacing video information, comprising:  
transmitting display type information of the video signal in at least one of  
the horizontal sync signal and the video signal to a monitor; and  
detecting a display type of the transmitted video signal using the display type  
5 information.

15. The method as claimed in claim 14, wherein the main body synchronizes the display type information with the vertical sync signal.

16. The method as claimed in claim 14, wherein a vertical sync signal comprises a clock pulse for recognizing the display type information.

17. A method of interfacing video information, comprising:  
dividing display type information of a video signal;  
embedding the divided display type information into the horizontal sync  
signal and the vertical sync signal, respectively;

5 transmitting to a monitor the horizontal and vertical sync signals having the divided display type information, along with the video signal;

decoding and reassembling the display type information; and

detecting a display type of the transmitted video signal using the reassembled display type information.

18. The method as claimed in claim 17, wherein the main body synchronizes the display type information with the vertical sync signal.

19. The method as claimed in claim 17, wherein a clock pulse for recognizing the display type information is included in the vertical sync signal.

20. A method of interfacing video information, comprising:

transmitting display type information of a video signal from a monitor to a main body;

receiving the display type information and converting a type of video signal based on the received information to match a display type of the monitor; and

displaying on the monitor the video signal transmitted from the main body.

21. The method as claimed in claim 20, wherein the display type information of the video signal that can be displayed by the monitor is a factory mode predetermined during a manufacturing process of the monitor.

22. The device of claim 1, wherein the display information comprises a number of dots for a horizontal period, a number of backporches for the horizontal period, a number of horizontal lines for a vertical period, and a number of horizontal lines of a backporch for the vertical period.

23. The device of claim 1, wherein the video signal comprises a RGB signal, a horizontal sync signal, and a vertical sync signal.

24. The device of claim 1, wherein the display information is transmitted in serial data.

25. An apparatus for interfacing video information, comprising:  
a monitor which transmits information regarding a display type of a video signal through a display data channel; and

a computer which converts the display type of the video signal based on the  
5 information received from the monitor through the display data channel and transmitting  
the converted video signal with horizontal and vertical sync signals to the monitor.

26. An apparatus for interfacing video information, comprising:

a computer transmitting horizontal and vertical sync signals, serial data  
signal and serial clock signals through a display data channel, and a video signal; and

a monitor receiving the horizontal and vertical sync signals, serial data signal  
and serial clock signals through the display data channel, and the video signal, wherein a  
display type information of the video signal is included in one of the serial data signal of  
the display data channel and the horizontal sync signal.

## ABSTRACT OF THE DISCLOSURE

An apparatus and method of interfacing video information which can provide an accurately displayed video picture irrespective of the type of a video input signal by interfacing the video display information between a main body and a monitor, and thus maintaining the optimum picture state. The apparatus includes a main body for outputting a video signal and information on a display type of the video signal, and a monitor for detecting the display type of the corresponding video signal in accordance with the display type information outputted from the main body and displaying on a display screen the video signal outputted from the main body to match the detected display type.



FIG.1  
Related Art

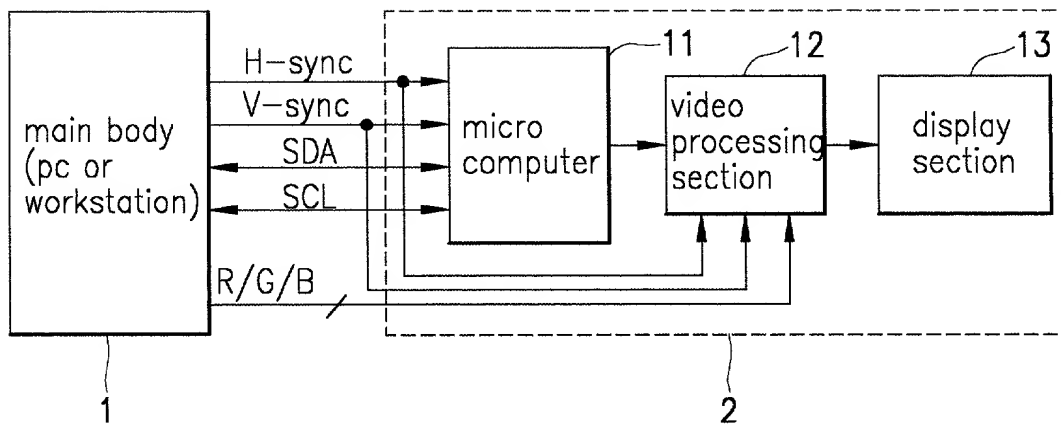


FIG.2  
Related Art

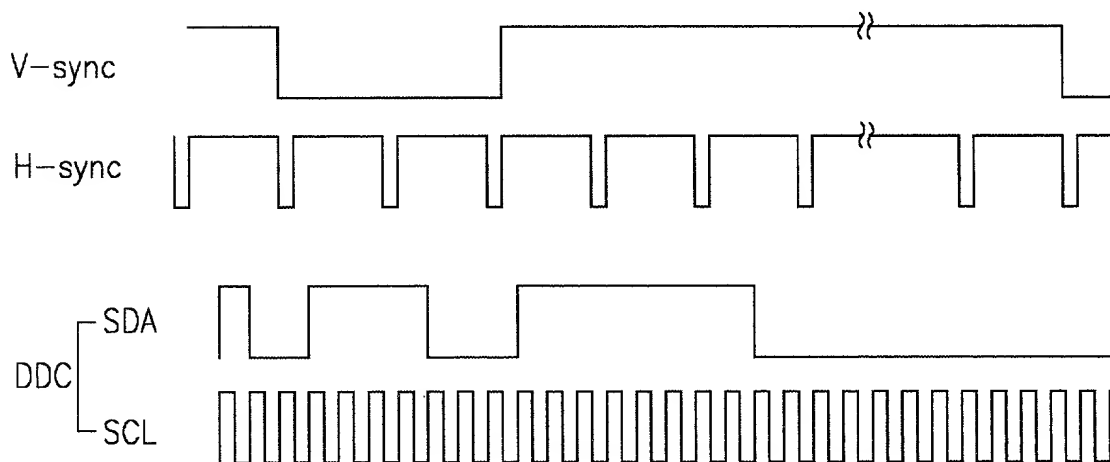


FIG.3

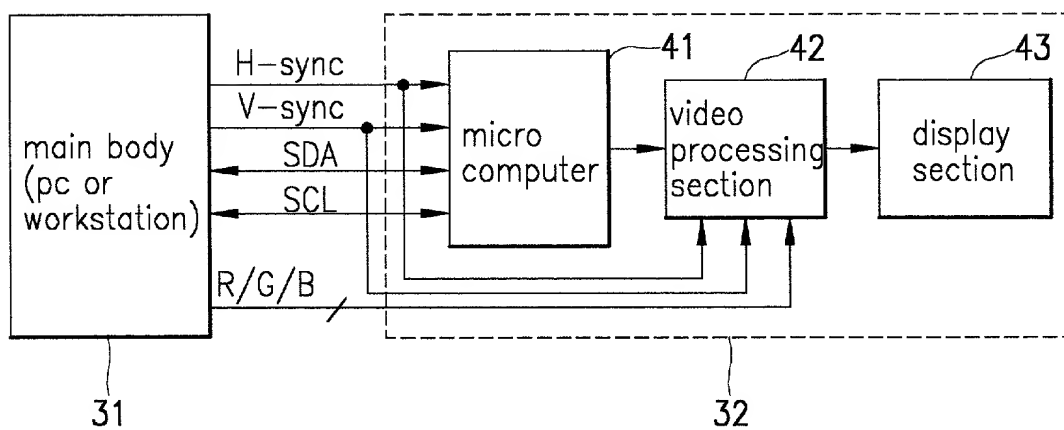


FIG.4

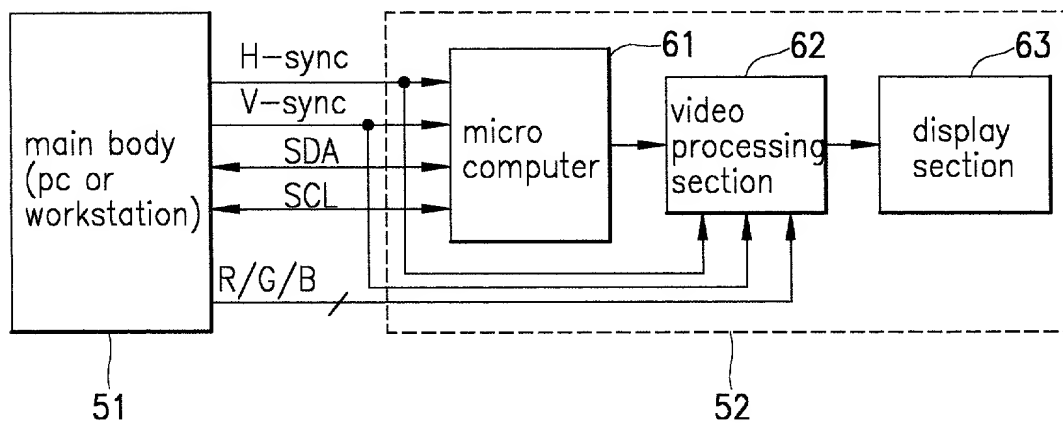


FIG.5

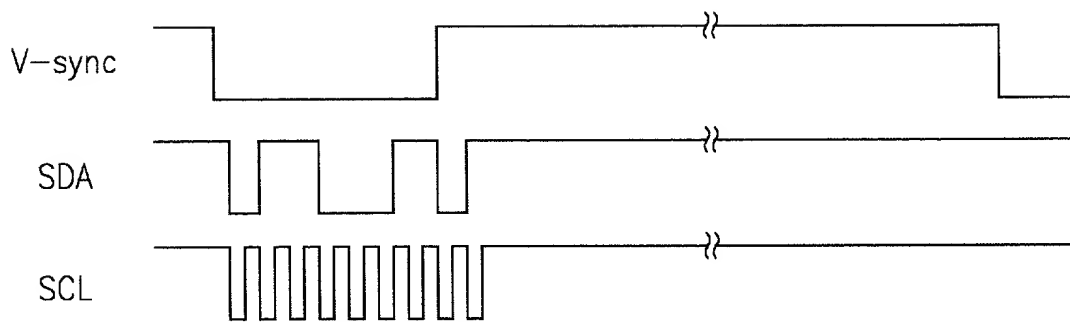


FIG.6

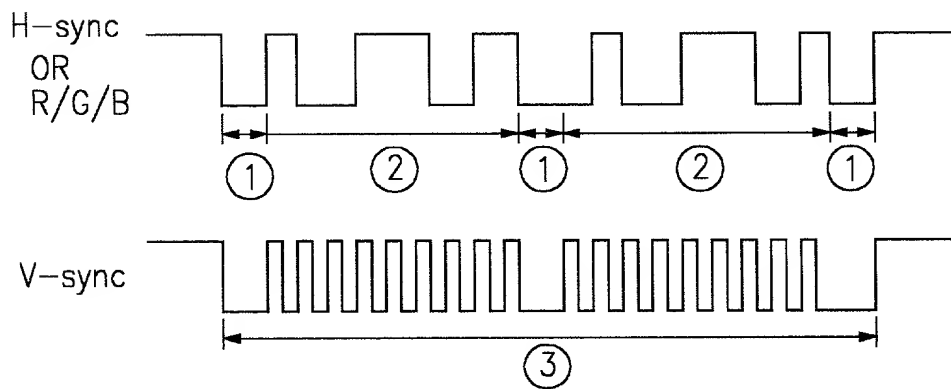


FIG.7

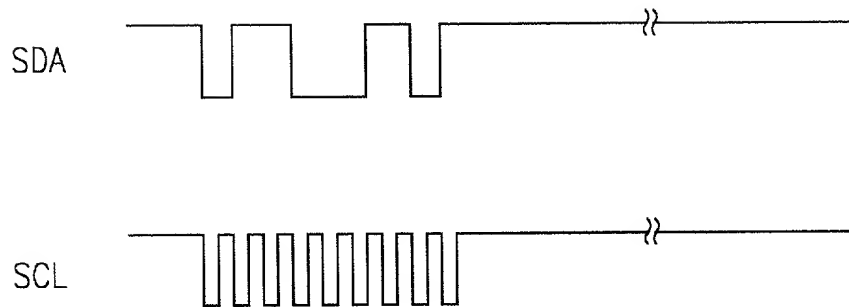
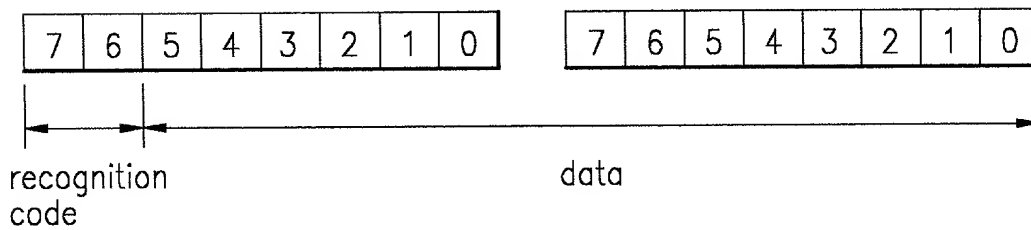


FIG.8



recognition code		data
bit 7	bit 6	bit 5 ~ bit 0    bit 7 ~ bit 0
0	0	the number of dots in a horizontal period
0	1	the number of backporches in a horizontal period
1	0	the number of horizontal lines in a vertical period
1	1	the number of horizontal lines of backporch in a vertical period

Docket No.: K-191

## DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

My residence, post office and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter claimed and for which a patent is sought on the invention entitled APPARATUS AND METHOD OF INTERFACING VIDEO INFORMATION IN A COMPUTER SYSTEM, the specification of which

☒ [X] is attached hereto      ☐ [ ] was filed on \_\_\_\_\_ as Application Serial No. \_\_\_\_\_ and was amended on \_\_\_\_\_ (if applicable)

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is known to me to be material to patentability in accordance with Title 37, Code of Federal Regulations, Section 1.56(a).

I hereby claim foreign priority benefits under 35 U.S.C. 119(a)-(d) or 365 (b) of any foreign application(s) for patent or inventor's certificate, or 365(a) of any PCT international application which designated at least one country other than the United States of America, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or of any PCT international application having a filing date before that of the application on which priority is claimed.

**Prior Foreign Application(s):**

**Number**

**Country**

**Foreign Filing Date**

**Month/Day/Year**

P1999-31588

Republic of Korea

July 31, 1999

I hereby claim the benefit under 35 U.S.C. 119(e) of any United States provisional application(s) listed below.

**Application Number(s):**

**Filing Date (Month/Day/Year)**

I hereby claim the benefit under 35 U.S.C. 120 of any United States application(s), or 365(c) of any PCT international application designating the United States of America, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT international application in the manner provided by the first paragraph of 35 U.S.C. 112, I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application.

**Prior U. S. Application  
or PCT Parent Number**

**Filing Date (Month/Day/Year)**

**Parent Patent Number (if applicable)**

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

I hereby appoint the following attorney(s) and/or agent(s): Daniel Y.J. Kim, Registration No. 36,186 and Mark L. Fleshner, Registration No. 34,596; Carl R. Wesolowski, Registration No. 40,372, John C. Eisenhart, Registration No. 38,128, Stuart I. Smith, Registration No. 42,159; Carol L. Druzick, Registration No. 40,287; Anthony H. Nourse, Registration No. 46,121; and Margaret A. Burke, Registration No. 34,474, all of

FLESHNER & KIM, LLP  
P. O. Box 221200  
Chantilly, Virginia 20153-1200

with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith, and all future correspondence should be addressed to them.

\*\*\*\*\*

Full name of sole or first inventor: Myoung Jun SONG

Inventor's signature: *mj song*

Date: 2000.7.26

Residence: Taegu-shi, Korea

Citizenship: Republic of Korea

Post Office Address: 1317-11, Kwanum-dong, Puk-gu, Taegu-shi, Korea

Full name of joint inventor(s):

Inventor's signature:

Date:

Residence:

Citizenship:

Post Office Address:

Full name of joint inventor(s):

Inventor's signature:

Date:

Residence:

Citizenship:

Post Office Address: